

jh240074

Joint Usage / Research Center for Interdisciplinary Large-scale Information Infrastructures

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Energy Efficient Operation for Supercomputer Systems

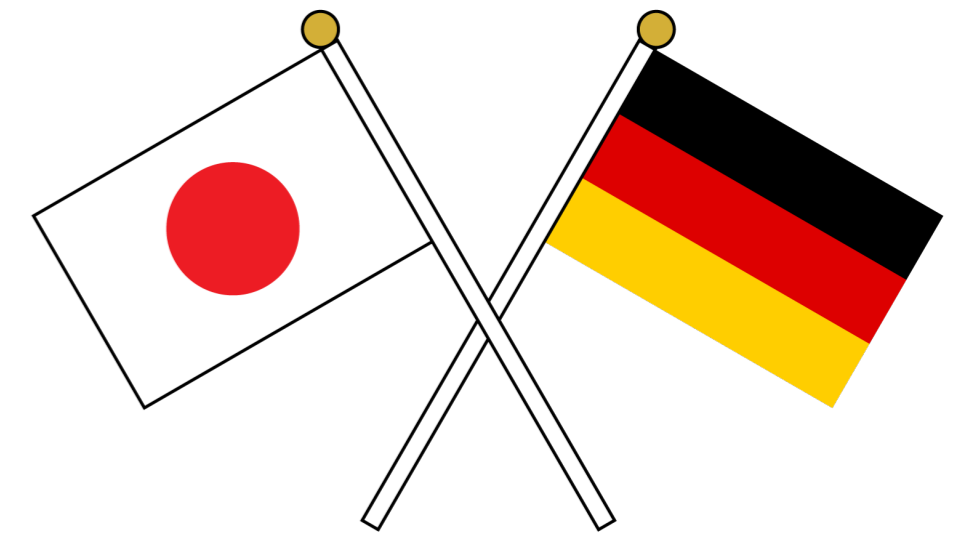
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Introduction

- Higher operation cost is severe issue for Supercomputer centers.
- In near future, Carbon footprint must be reduced and realized Net-0.
- The performance of the supercomputer systems in Japan will be able to be compared with that of German supercomputer centers, including the difference in climate and the performance profile of the advanced high-temperature water cooling system.

Purpose

- Explore optimal energy-efficient operation methods for the supercomputer system
- Supercomputer systems in operation are measured using benchmarks and real applications, and energy consumption and carbon footprint are modeled according to those results.
- Clock frequency should be appropriately controlled for the processors (CPU & GPU) at frequency suitable for processing at the time.



Resource

Japan		Germany (warm water cooling)	
U. Tokyo	Wisteria/BDEC-01 (Odyssey: A64FX, Aquarius: ICX+A100 GPU), Miyabi (OFP-II, 2025/1-)	RWTH Aachen	SPR+H100 GPU (PUE monitoring)
Tokyo Tech	TSUBAME 4.0 (Gemini+H100 GPU)	Paderborn U.	Noctua-1, Noctua-2 (A100, FPGA), NHR-1 (GPU)
Kyoto U.	Camphor 3 (SPR HBM)	LRZ	SuperMUC-NG (Intel SPR+PVC)
Osaka U.	SQUID (ICX+A100 GPU, SX-Aurora Tsubasa)	TU Dresden	Under planning

Theme

1. Study on the usage of low-precision calculation and for energy reduction (Sakamoto, Hanawa, Nakajima, Fukaya, Kataqiri, Ohshima, Wellein)-

- The power efficiency improvement by aggressively using low-precision arithmetic for HPC applications will be studied
- In collaboration with **jh240021 project** for target applications, we will investigate energy profiles using various architectures on both Japanese and German computing resources.

2. Study on analysis corresponding among cooling, energy consumption, and performance

(Hanawa, Sumimoto, Miki, Ohara, Nomura, Nonaka, Miura, Shoji, Terai, Plessl, Simon, Rohde, Ilsche, Huber, Müller)

- To lower the operating costs of supercomputing systems → Higher temperature cooling water should be used and ideally only cooling tower without chiller system
- Higher operating temperature may impact the CPU power consumption due to the leakage current, and it may degrade performance due to DVFS (Dynamic Voltage-Frequency Scaling) mechanism activated to maintain the power and temperature within TDP (Thermal Design Power) limit.
- We will directly compare such systems using different cooling methods in Germany and Japan.

3. Study on parameterizing optimal configuration based on application performance

(Yamazaki, Shimokawabe, Hanawa, Takizawa, Endo, Kawai, Fukazawa, Nanri, Miwa, Yoshida, Kusaba, Honda, Eitzinger, Müller, Terboven)

- Variations on performance and power consumption for each processors can be observed even in the same cluster [2].
 - Variations are strongly affected by both the kernels that are running and the process generation [5].
- According to the algorithms, some processor cores could be set to sleep to save energy and allow better use of the power and thermal budget [7]. In collaboration with **jh240072 project**, optimal parameters for parallelization can be determined dynamically.
- Evaluate kernel characteristics in benchmarks and applications using various kinds of architectures, analyze to classify them into CPU-bound, memory-bound, communication-bound, IO-bound, etc., and parameterize to determine optimal configuration for energy efficiency.

Reference

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